

EDUCATION

- **University of Michigan** Ann Arbor, MI, U.S.A
 - *MS Electrical and Computer Engineering ; Integrated Circuits & VLSI* *Aug 2021 – December 2022(Expected)*
 - **Coursework:** Programming Languages(EECS 590), Parallel Computer Architecture (EECS 570), VLSI Design I&II (EECS 427,627),Digital Integrated Technology (EECS 523),Intr. Embedded Systems Research (EECS 507), Quantum Integrated Technology(EECS 427),Quantum Information, Probability and Computation(EECS 598).
- **IIT Madras - Pennsylvania State University - MHRD-GIAN Course** Chennai, TN, India
 - *Short Term Course :: Emerging Computational Devices, Architectures and Computational Models* *December 2017*
- **Shiv Nadar University** Dadri, UP, India
 - *Bachelor of Technology in Electronics and Communication Engineering* *Aug 2014 – May 2018*
 - **Relevant Coursework:** Digital System Design With FPGAs, Embedded Systems Hardware, Operating Systems, Data Structures & Algorithms, Digital Signal Processing, Applied Machine Learning, Brain Sciences and Cognition, Communication Engineering, Computer Networks, Mathematical Methods, Control Systems, Electromagnetic Engineering, VLSI Technology and Design, Design of Analog CMOS Circuits, Information Theory& Coding, Network Analysis.

EXPERIENCE

- **Apple Inc.** Cupertino , CA, USA
 - *CPU Design Intern* *May 2022 - Aug 2022*
- **Ventana Microsystems** Bengaluru, KA, India
 - *Staff Engineer* *Jan 2021 - Aug 2021*
- **RISE-Group IIT MADRAS** Chennai, TN, India
 - *Project Associate - Ministry of E&IT - India Microprocessor Development Program,Intern(2017)* *May 2018 - Aug 2020*

TECHNICAL SKILLS

System Verilog (+UVM), Verilog , Bluespec SV, Digital Front End (Synopsys,Cadence) , FPGA - Xilinx, ASM(RISC-V,ARM), C, C++, Python, Perl, Linux, GDB, OpenOCD, Matlab , [Virtuoso , Xyce, KLayout, Magic], CUDA, Basic Data Science , \LaTeX