Paul George

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EDUCATION

SHIV NADAR UNIVERSITY

JUNIOR

B.Tech in Electronics and Communications Engineering

Dadri, India Expected May 2018 Cum. GPA: 8.4 / 10.0 Major GPA: 8.6 / 10.0

SOMERVILLE SCHOOL NOIDA

Grad. April 2014 | Noida, India AISSCE | 12th Grade | 89 / 100 Problem Solving Assessment Percentile Field 99.2136 Language Conventions 91.8784 Qualitative Reasoning 99.7484 Quantitative Reasoning CBSE | CCE | GPA: 8.2 / 10.0

- Member | Student Council
- Head | Quizzing Society

LINKS

Keybase.io:// paulgeorge GitHub:// command-paul Facebook:// command.paul Quora:// Paul-George-5 Reddit:// commandpaul

ACADEMIC INTERESTS

Computer Architecture Digital Signal Processing Networks

- Hardware Accelerators
- Electrophysiology
- Data Analysis
- Machine Learning

EXPERIENCE

SHIV NADAR UNIVERSITY RESEARCH & TEACHING ASSISTANT

May / 2016 - July / 2016 |Department of Electrical Engineering|

- Worked on the RNBIP Processor(Description below)
- Teaching Assistant for the Summer offering of the Introduction to Electrical Engineering Course by Professor R.N.Biswas.

HCL INFOSYSTEMS LTD. Systems Design Engineer - Intern :: Hardware Research and Development

June / 2015 - July / 2015 |Internet of things|

- Worked on multiple projects involving the development of customized IOT implementations for the automotive and health care sector .
- Worked with ARMv4 SOC's, a variety of sensors, and communication channels.

PROJECTS

RNBIP - R.N.BISWAS IP DIGITAL DESIGN | TEAM LEAD

Summer Project | Shiv Nadar University

- Developed an 8 Bit Pipelined Processor
- Deployed the same on an FPGA Xilinx Artix
- Developed a Parallelised Emulator for use as a teaching aid.

BLIP - BLUETOOTH BASED INDOOR POSITIONING Hackathon

IOT | Open Source | Hacker Earth INDIA Hacks

- Developed a Bluetooth based indoor positioning system to enable context based services and asset tracking in indoor spaces
- Developed and implemented a trilateration algorithm to achieve the same.
- Position Resolution at 1M.

EARTHQUAKE EARLY WARNING SYSTEM SNU - EEWS

Centre for Urban studies and Disaster Management | Shiv Nadar University

- Developing a cost effective distributed sensor network
- Also Working on DSP Feature Implementations to aid classification
- Work in Progress.

ELECTROMYOGRAPHY AND ACTIVITY TRACKING

ELECTROMYOGRAPH | TEAM LEAD

Course Project | Signals and Systems | Shiv Nadar University

- Developed a Bluetooth Wireless Multi-channel Electromyograph
- Data recorded was used to establish relations between the EMG and the physical position of the limb.

GRAPH WIZ Spring 2015 | Semester II

Open Source | Teaching Aid | Data Structures and Algorithms

- Developed a teaching aid for the Gnu-Linux platform of man pages and libraries describing and implementing popular data structures and algorithms.
- A Graph visualization was tool developed to sketch graphs using open GL.

COURSE WORK

UNDERGRADUATE

Selected Courses
Digital System Des. With FPGAs
Embedded Systems Hardware
Operating Systems
Data Structures
Signals and Systems
Digital Signal Processing

Mobile & Wireless Technology Applied Machine Learning Brain Sciences and Cognition Quan. Techniques In Business

Financial Frauds Control Systems

Communication Networks Intro. to Science of Networks Mathematical Methods Electromagnetic Engineering

Electromechanics Power Electronics Semiconductor Devices VLSI Technology and Design

PROGRAMMING

Over 5000 lines:

Verilog • C • C++ • Python

Over 2000 lines:

Nvidia - CUDA • BASH

 $\bullet \mathsf{MATLAB}/\mathsf{OCTAVE} \bullet \mathsf{Assembly}$

Comfortably Familiar:

MFX .

AWARDS

Year Description

2015 215 (/335) ACM ICPC Amrita Multisite Regional

2016 7th (National) Hackerearth - India Hacks IOT Hackathon - Finals

SOCIETIES

2014 - Collegiate ACM-Student Chapter Shiv Nadar University
 2014 - Collegiate IEEE Student Chapter @ Shiv Nadar University
 2014 - Collegiate Robotics Society Shiv Nadar University

REFERENCES

Prof R.N.Biswas Department of Electrical Engineering | Shiv Nadar University

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Mr.Bharnidhar Naidu Senior Manager | R&D | HCL INFOSYSTEMS ltd.

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Prof. Atul Vir Singh Department of Electrical Engineering | Shiv Nadar University

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